CTR

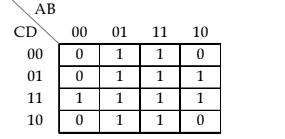
CA QUIZ

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1)The Boolean function with the Karnaugh map



**(A)** (A+C).D+B

**(B)** (A+B).C+D

**(C)** (A+D).C+B

**(D)** (A+C).B+D

2)The device which is used to connect a peripheral to bus is known as

**(A)** control register

**(B)** interface

**(C)** communication protocol

**(D)** none of these

3)What is the minimum size of ROM required to store the complete truth table of an 8-bit x 8-bit multiplier?

**(A)** 32 K x 16 bits

**(B)** 64 K x 16 bits

**(C)** 16 K x 32 bits

**(D)** 64 K x 32 bits

4)How many 128 × 8 bit RAMs are required to design 32 K × 32 bit RAM?

**(A)** 512

**(B)** 1024

**(C)** 128

**(D)** 32

5)More than one word are put in one cache block to

**(A)** exploit the temporal locality of reference in a program

**(B)** exploit the spatial locality of reference in a program

**(C)** reduce the miss penalty

**(D)** none of the above

6)The amount of ROM needed to implement a 4 bit multiplier is

**(A)** 64 bits

**(B)** 128 bits

**(C)** 1 Kbits

**(D)** 2 Kbits

7)The \_\_\_\_\_ addressing mode is similar to register indirect addressing mode, except that an offset is added to the contents of the register. The offset and register are specified in the instruction.

**(A)** Base indexed

**(B)** Base indexed plus displacement

**(C)** Indexed

**(D)** Displacement

8)Register renaming is done in pipelined processors

**(A)** as an alternative to register allocation at compile time

**(B)** for efficient access to function parameters and local variables

**(C)** to handle certain kinds of hazards

**(D)** as part of address translation

9)Relative mode of addressing is most relevant to writing

**(A)** co-routines

**(B)** position-independent code

**(C)** shareable code

**(D)** interrupt handerls

10)How many 32K X 1 RAM chips are needed to provide a memory capacity of 256K-bytes?

**(A)** 8

**(B)** 32

**(C)** 64

**(D)** 128

KEYS

1. A
2. B
3. B
4. B
5. B
6. D
7. D
8. C
9. B
10. C